Form C: Type Test Verification Report

All Micro-generators connected to the **DNO Distribution Network** shall be **Fully Type Tested**. This form is the **Manufacturer**'s declaration of compliance with the requirements of EREC G98.

This form should be used when making a Type Test submission to the Energy Networks Association (ENA) Type Test Register.

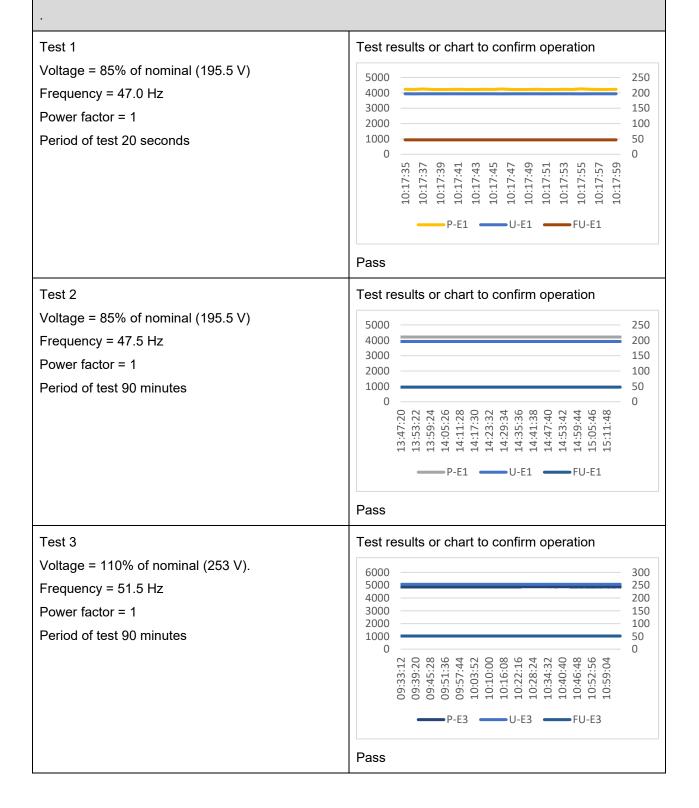
If the **Micro-generator** is **Fully Type Tested** and already registered with the ENA Type Test Register, the **Installation Document** should include the **Manufacturer**'s Reference Number (the system reference), and this form does not need to be submitted.

	<u> </u>		<u> </u>				
Manufacture	er's referenc	ce number	RI-Energyf	low-P3-Series 1	5 - 5.00kW		
Micro-gener	r ator techno	logy	Grid-tied ph	otovoltaic inverte	r		
Manufacture	er name		Rayleigh Ins	Rayleigh Instruments LTD			
Address				House, Cutlers roa ,Essex. England	ad, South Woodham Ferrers,		
Tel	Grid-tied ph	notovoltaic inver	rter	Fax	01245 428509		
E-mail	Rayleigh Ins	struments LTD		Web site	www.Rayleigh.com		
		Connection C	Option				
Registered use separate			kW single phase, single, split or three phase system				
more than one connection option.		5	kW three phase				
			kW two phases in three phase system				
			kW two phases split phase system				
Energy stora capacity for I Storage dev	Electricity		kWh				
Fully Type stated in this	Tested refer document,	rence number	will be man ent to site an	ufactured and to d that no site mo	pplied by the company with the above ested to ensure that they perform as odifications are required to ensure that		
Signed		On behalf of		Rayleigh Instruments Limited			
Note that tes house.	sting can be	done by the	Manufactur	er of an individu	ual component or by an external test		

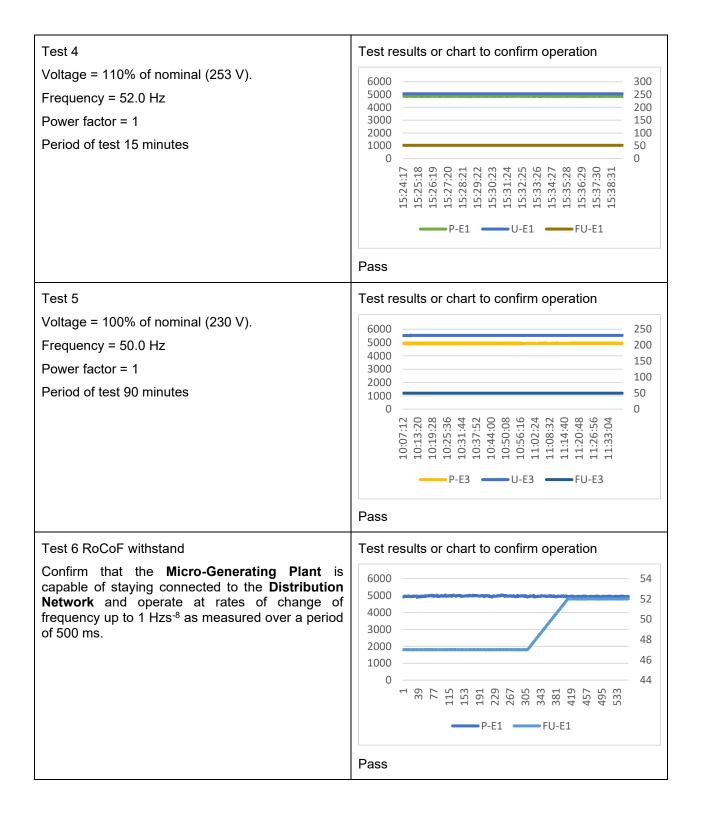
Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.

Operating Range: This test should be carried out as specified in A.1.2.10.

Pass or failure of the test should be indicated in the fields below (right hand side), for example with the statement "Pass", "No disconnection occurs", etc. Graphical evidence is preferred.



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Power Quality – Harmonics: These tests should be carried out as specified in BS EN 61000-3-2. The chosen test should be undertaken with a fixed source of energy at two power levels a) between 45 and 55% and b) at 100% of **Registered Capacity**. The test requirements are specified in Annex A1 A.1.3.1 (**Inverter** connected) or Annex A2 A.2.3.1 (Synchronous).

•		,					,			
			Mic	ro-	generato	r tested t	OBSEN6	510	00-3-2	_
Micro-		or rating rpp)	per phas	e	1.66				kW	
measure harmoni	ements a	are identi ot identic	l thi ch i	this box if ee phase ohase, ple ohase.	es. If the					
Harmo nic						% of Reg Capac				
	Measu Amps	Measured Value MV in Amps			Measured Value MV in Amps				Limit in BS EN 61000-3-2 in Amps	Higher limit for odd harmonics 21 and above
2	0.104	0.105	0.107		0.105	0.106	0.105		1.080	
3	0.091	0.091	0.093		0.091	0.092	0.091		2.300	
4	0.062	0.061	0.063		0.062	0.061	0.061		0.430	
5	0.169	0.164	0.166		0.166	0.162	0.161		1.140	
6	0.007	0.008	0.007		0.008	0.007	0.008		0.300	
7	0.048	0.048	0.046		0.045	0.046	0.046		0.770	
8	0.004	0.006	0.005		0.005	0.004	0.005		0.230	
9	0.031	0.031	0.031		0.030	0.031	0.031		0.400	
10	0.033	0.034	0.033		0.033	0.033	0.032		0.184	
11	0.031	0.028	0.030		0.030	0.030	0.029		0.330	
12	0.019	0.019	0.019		0.019	0.020	0.017		0.153	
13	0.064	0.066	0.062		0.061	0.061	0.066		0.210	
14	0.023	0.025	0.023		0.023	0.024	0.024		0.131	
15	0.012	0.011	0.013		0.012	0.012	0.014		0.150	
16	0.015	0.017	0.016		0.016	0.017	0.015		0.115	

¹ See the note in A.2.3.1 if 45-55% of **Registered Capacity** is below the minimum stable operating level. If an alternative loading level is chosen, the level should be indicated on the test form and the reason for not testing at 45-55% of **Registered Capacity** should be stated. The additional comments box at the end of the harmonics test sheet can be used for this.

17	0.041	0.040	0.040	0.	039	0.041	0.039	0.132	
18	0.009	0.008	0.009	0.	010	0.009	0.008	0.102	
19	0.013	0.013	0.015	0.	015	0.014	0.014	0.118	
20	0.009	0.008	0.009	0.	007	0.008	0.008	0.092	
21	0.004	0.004	0.005	0.	004	0.004	0.005	0.107	0.160
22	0.006	0.007	0.007	0.	007	0.006	0.006	0.084	
23	0.014	0.013	0.013	0.	013	0.011	0.013	0.098	0.147
24	0.002	0.003	0.003	0.	002	0.003	0.002	0.077	
25	0.012	0.013	0.013	0.	014	0.013	0.012	0.090	0.135
26	0.004	0.004	0.004	0.	005	0.004	0.004	0.071	
27	0.004	0.002	0.003	0.	003	0.004	0.003	0.083	0.124
28	0.004	0.004	0.003	0.	003	0.004	0.002	0.066	
29	0.006	0.007	0.006	0.	005	0.006	0.007	0.078	0.117
30	0.003	0.002	0.003	0.	004	0.004	0.002	0.061	
31	0.004	0.003	0.003	0.	003	0.004	0.003	0.073	0.109
32	0.004	0.004	0.004	0.	005	0.004	0.004	0.058	
33	0.004	0.003	0.004	0.	004	0.004	0.003	0.068	0.102
34	0.002	0.002	0.002	0.	002	0.003	0.001	0.054	
35	0.006	0.006	0.004	0.	005	0.006	0.005	0.064	0.096
36	0.001	0.001	0.001	0.	001	0.001	0.001	0.051	
37	0.004	0.004	0.003	0.	004	0.003	0.003	0.061	0.091
38	0.002	0.003	0.004	0.	003	0.002	0.002	0.048	
39	0.004	0.003	0.004	0.	003	0.004	0.003	0.058	0.087
40	0.001	0.004	0.001	0.	002	0.002	0.002	0.046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

Additional comments:

Power Quality – Voltage fluctuations and Flicker: These tests should be undertaken in accordance with EREC G98 Annex A1 A.1.3.3 (**Inverter** connected) or Annex A2 A.2.3.3 (Synchronous).

The standard test impedance is 0.4Ω for a single phase **Micro-generating Plant** (and for a two phase unit in a three phase system) and 0.24Ω for a three phase **Micro-generating Plant** (and for a two phase unit in a split phase system). Please ensure that both test and standard impedance are completed on this form. If the test impedance (or the measured impedance) is different to the standard impedance, it must be normalised to the standard impedance as follows (where the **Power Factor** of the generation output is 0.98 or above):

d max normalised value = (Standard impedance / Measured impedance) x Measured value.

Where the **Power Factor** of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the standard impedance.

The stopping test should be a trip from full load operation.

The duration of these tests needs to comply with the particular requirements set out in the testing notes for the technology under test.

The test date and location must be declared.

Test start date	23 May.	23 May.,2022			23 May.,2022				
Test location									
	Starting			Stopping	Stopping			Running	
	d(max)	d(c)	d(t)	d(max)	d(c)	d(t)	Pst	Plt 2 hours	
Measured Values at test impedance	0.41 %	0.11 %	0%	0.51%	0.1%	0%	0.18	0.15	
Normalised to standard impedance	0.41 %	0.11 %	0%	0.51%	0.1%	0%	0.18	0.15	
Normalised to required maximum impedance	0.41 %	0.11 %	0%	0.51%	0.1%	0%	0.18	0.15	
Limits set under BS EN 61000-3-11	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65	

Test Impedance	R	0.24	Ω	Х	0.15	Ω
Standard Impedance	R	0.24 * 0.4 ^	Ω	х	0.15 * 0.25 ^	Ω
Maximum Impedance	R	0.24	Ω	х	0.15	Ω

*Applies to three phase and split single phase **Micro-generators**. Delete as appropriate.

^ Applies to single phase **Micro-generators** and **Micro-generators** using two phases on a three phase system. Delete as appropriate.

Power quality – DC injection: This test should be carried out in accordance with A 1.3.4 as applicable.

The % **DC** injection ("as % of rated AC current" below) is calculated as follows:

% **DC** injection = Recorded **DC** value in Amps / base current

where the base current is the **Registered Capacity** (W) / 230 V. The % **DC** injection should not be greater than 0.25%.

Test power level	20%	50%	75%	100%
Recorded DC value in Amps	0.011	0.002	0.012	0.015
as % of rated AC current	0.14%	0.03%	0.15%	0.19%
Limit	0.25%	0.25%	0.25%	0.25%

Power Quality – Power factor: This test shall be carried out in accordance with A.1.3.2 and A.2.3.2 at three voltage levels and at **Registered Capacity** and the measured **Power Factor** must be greater than 0.95 to pass. Voltage to be maintained within ±1.5% of the stated level during the test.

	216.2 V	230 V	253 V
Measured value	0,9978	0,9975	0,9975
Power Factor Limit	>0.95	>0.95	>0.95

(Inverter con	• •	A.2.2.3 (Synchronous). Fo	ed out in accordance with Annex A1 A.1.2.3 or trip tests, frequency and time delay should
Function	Setting	Trip test	"No trip tests"

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	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F stage 1	47.5 Hz	20 s	47.47Hz	19.99s	47.7 Hz 30 s	no trip
U/F stage 2	47 Hz	0.5 s	46.91Hz	0.512s	47.2 Hz 19.5 s	no trip
					46.8 Hz 0.45 s	no trip
O/F stage 1	52 Hz	0.5 s	52.1Hz	0.513s	51.8 Hz 120.0 s	no trip
					52.2 Hz 0.45 s	no trip

Note. For frequency trip tests the frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

Protection – Voltage tests: These tests should be carried out in accordance with Annex A1 A.1.2.2 (**Inverter** connected) or Annex A2 A.2.2.2 (Synchronous). For trip tests, voltage and time delay should be stated. For "no trip tests", "no trip" can be stated.

Function	Setting	Setting			"No trip tests"		
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip	
U/V	184 V	2.5 s	182.4V	2.48s	188 V 5.0 s	no trip	
					180 V 2.45 s	no trip	
O/V stage 1	262.2 V	1.0 s	265.1V	1.012S	258.2 V 5.0 s	no trip	
O/V stage 2	273.7 V	0.5 s	275.3V	0.511S	269.7 V 0.95 s	no trip	
					277.7 V 0.45 s	no trip	
Ph2							
Eurotion Setting			Trin test		"No trip tests"		

Function	Setting		Trip test		"No trip tests"		
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip	
U/V	184 V	2.5 s	182.4V	2.49s	188 V 5.0 s	no trip	
					180 V 2.45 s	no trip	
O/V stage 1	262.2 V	1.0 s	265.6V	0.99S	258.2 V 5.0 s	no trip	

O/V stage 2	273.7 V	0.5 s	275.9V	0.51S	269.7 V 0.95 s	no trip				
					277.7 V 0.45 s	no trip				
Ph3	Ph3									
Function	Setting		Trip test		"No trip tests"					
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip				
U/V	184 V	2.5 s	183V	2.53s	188 V 5.0 s	no trip				
					180 V 2.45 s	no trip				
O/V stage 1	262.2 V	1.0 s	264.5V	1.02S	258.2 V 5.0 s	no trip				
O/V stage 2	273.7 V	0.5 s	275.1V	0.485S	269.7 V 0.95 s	no trip				
					277.7 V 0.45 s	no trip				

Note for Voltage tests the Voltage required to trip is the setting ± 3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ± 4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

Protection – Loss of Mains test: For PV **Inverter**s shall be tested in accordance with BS EN 62116. Other **Micro-generators** should be tested in accordance with A.2.2.4 at 10%, 55% and 100% of rated power.

To be carried out at three output power levels with a tolerance of plus or minus 5% in Test Power levels. ²
To be defined out at three output power levels with a tolerance of plus of minus 0.76 in rest rower levels.

Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Limit is 0.5 s	N/A	N/A	N/A	N/A	N/A	N/A

For Multi phase **Micro-generators** confirm that the device shuts down correctly after the removal of a single fuse as well as operation of all phases.

Test Power10%55%	100%	10%	55%	100%
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² See the note in A.2.2.4 if the suggested loading levels are below the minimum stable operating level. If alternative loading levels are chosen, the level should be indicated on the test form and the reason for not testing at 10%/55% of **Registered Capacity** should be stated. The additional comments box at the end of the loss of mains test sheet can be used for this.

Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph1 fuse removed	N/A	N/A	N/A	N/A	N/A	N/A
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph2 fuse removed	N/A	N/A	N/A	N/A	N/A	N/A
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph3 fuse removed	N/A	N/A	N/A	N/A	N/A	N/A

Note for technologies which have a substantial shut down time this can be added to the 0.5 s in establishing that the trip occurred in less than 0.5 s. Maximum shut down time could therefore be up to 1.0 s for these technologies.

Indicate additional shut down time included in above results.

ms

N/A

Additional comments:

For **Inverter**s tested to BS EN 62116 the following sub set of tests should be recorded in the following table.

Test Power and	33%	66%	100%	33%	66%	100%
imbalance	-5% Q	-5% Q	-5% P	+5% Q	+5% Q	+5% P
	Test 22	Test 12	Test 5	Test 31	Test 21	Test 10
Trip time. Limit is 0.5 s^3	215ms	199ms	198ms	216ms	189ms	192ms

Protection – Frequency change, Vector Shift Stability test: This test should be carried out in accordance with EREC G98 Annex A1 A.1.2.6 (**Inverter** connected) or Annex A2 A.2.2.6 (Synchronous). Confirmation is required that the **Micro-generating Plant** does not trip under positive / negative vector shift.

³ If the device requires additional shut down time (beyond 0.5 s but less than 1 s) then this should be stated on this form.

	Start Frequency	Change	Confirm no trip
Positive Vector Shift	49.0 Hz	+50 degrees	no trip
Negative Vector Shift	50.0 Hz	- 50 degrees	no trip

Protection – Frequency change, RoCoF Stability test: The requirement is specified in section 11.3, test procedure in Annex A.1.2.6 (**Inverter** connected) or Annex A2 A.2.2.6 (Synchronous). Confirmation is required that the **Micro-generating Plant** does not trip for the duration of the ramp up and ramp down test.

Ramp range	Test frequency ramp:	Test Duration	Confirm no trip
49.0 Hz to 51.0 Hz	+0.95 Hzs ⁻¹	2.1 s	no trip
51.0 Hz to 49.0 Hz	-0.95 Hzs ⁻¹	2.1 s	no trip

Limited Frequency Sensitive Mode – Overfrequency test: This test should be carried out in accordance with A.1.2.8. The test should be carried out using the specific threshold frequency of 50.4 Hz and **Droop** of 10%. The measurement tolerances are contained in A.1.2.8.

Test sequence at Registered Capacity >80%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient
Step a) 50.00 Hz ±0.01 Hz	4991W	50Hz	630V/5200W	99.82%
Step b) 50.45 Hz ±0.05 Hz	4941W	50.45Hz		98.82%
Step c) 50.70 Hz ±0.10 Hz	4691W	50.7Hz		93.82%
Step d) 51.15 Hz ±0.05 Hz	4241W	51.15Hz		84.82%
Step e) 50.70 Hz ±0.10 Hz	4662W	50.7Hz		93.24%
Step f) 50.45 Hz ±0.05 Hz	4938W	50.45Hz		98.76%
Step g) 50.00 Hz ±0.01 Hz	4889W	50Hz		97.78%
Test sequence at Registered Capacity 40% - 60%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient
Step a) 50.00 Hz ±0.01 Hz	2512	50Hz	630V/5200W	50.24%
Step b) 50.45 Hz ±0.05 Hz	2462	50.45Hz		49.24%
Step c) 50.70 Hz ±0.10 Hz	2212	50.7Hz		44.24%
Step d) 51.15 Hz ±0.05 Hz	1762	51.15Hz		35.24%
Step e) 50.70 Hz ±0.10 Hz	2230	50.7Hz		44.60%
Step f) 50.45 Hz ±0.05 Hz	2465	50.45Hz		49.30%

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Step g) 50.00 Hz ±0.01 H	lz 25	08	50Hz				50.16%		
Power output with falling frequency test: This test should be carried out in accordance with A.1.2.7.									
Test sequence	Measured Power Output	Active It	Frequency		Primary power source				
Test a) 50 Hz ± 0.01 Hz		4980W		50Hz		5200W			
Test b) Point between 4 and 49.6 Hz	19.5 Hz	4915W		49.5Hz		5200W			
Test c) Point between 4 and 47.6 Hz	17.5 Hz	4952W		47.5Hz		5200W			
NOTE: The operating poi	nt in Tes	t (b) and (c) sh	all be m	aintained	l for at least 5	minutes			
6000					50.	.5			
5000	1				50				
4000					49				
3000					48.				
2000					47.	.5			
1000					47				
0 4 2 7 8 0 2 6 1	5 N 8 C	0 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	0 4 0 1	പരഒര	46				
13:50:34 13:51:15 13:51:15 13:51:57 13:52:38 13:52:38 13:52:38 13:52:38 13:52:4:02	13:55:25 13:56:07 13:56:48 13:56:48	13:58:11 13:58:53 13:59:35 14:00:16 14:00:58	4:01:4 4:02:2 4:03:0	14:03:45 14:04:26 14:05:08 14:05:49	14:06:31 14:07:12 14:07:53				
		P - FU		ਜ ਜ ਜ ਜ	। तं तं तं				
Re-connection timer.									
Test should prove that the voltage and frequency to measured delay should b should be provided that settings below; a stateme	o within t be provide the Micr	the stage 1 se ed in this form; o-generating	ettings o ; both sl Plant de	of Table 2 nould be oes not r	2. Both the ti greater than 2	me delay 20 s to pas	setting and the s. Confirmation		
Time delay Measured setting delay		Checks on just outside				or frequen	cy is brought to		
30S 62S		At 266.2 V	At 180).0 V	At 47.4 Hz	<u> </u>	At 52.1 Hz		
Confirmation that the generator does not re-co		- Not re- connect	not re	-connect	not re-con	nect	not re-connect		
Fault level contribution: (Inverter connected) and contribution is zero.									

For machines with electro-magnet	For Inverter output				
Parameter	Symbol	Value	Time after fault	Volts	Amps
Peak Short Circuit current	İ _p	N/A	20 ms	45V	11.5A
Initial Value of aperiodic current	A	N/A	100 ms	42V	0.29A
Initial symmetrical short-circuit current*	I _k	N/A	250 ms	42V	0.33A
Decaying (aperiodic) component of short circuit current*	i _{DC}	N/A	500 ms	42V	0.223A
Reactance/Resistance Ratio of source*	×/ _R	N/A	Time to trip	0.696s	In seconds

For rotating machines and linear piston machines the test should produce a 0 s - 2 s plot of the short circuit current as seen at the **Micro-generator** terminals.

* Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot

Logic Interface (input port)

Confirm that an input port is provided and can be used to reduce the **Active Power** output Yes to zero

Provide high level description of logic interface, e.g. details in 9.4.3 such as AC or **DC** signal (the additional comments box below can be used)

Self-Monitoring solid state switching: No specified test requirements. Refer to EREC N/A G98 Annex A1 A.1.3.6 (**Inverter** connected).

It has been verified that in the event of the solid state switching device failing to disconnect the **Micro-generator**, the voltage on the output side of the switching device is reduced to a value below 50 V within 0.5 s.

Cyber security

Confirm that the **Manufacturer** or **Installer** of the **Micro-generator** has provided a Yes statement describing how the **Micro-generator** has been designed to comply with cyber security requirements, as detailed in 9.7.

Additional comments

Logic Interface (input port):

the logic interface will take the form of a simple binary output. When the switch is opened the Microgenerator can operate normally. When the switch is closed the Microgenerator will reduce its Active Power to zero within 5 s. The signal from the Microgenerator that is being switched is DC 5 V.

Cyber security:

We used a communication server with an SSL certificate.